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Applicant: Shunpei YAMAMOTO, et al. INFORMATION DISCLOSURE STATEMENT

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(Use several sheets if necessary)			Filing Date: April 22, 1997		Group: 1104	S.	6
	U.S	. PATENT	DOCUMENTS			3 C	200
Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing D	ate opriate)
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	FORE	IGN PATE	NT DOCUMENTS				
	Document Number	Date	Country	Class	Subclass	Translation	
			<u> </u>	<u> </u>		Yes	No
							
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			<u> </u>			<u> </u>	
	OTHER DOCUMENTS	Including Auth	nor, Title, Date, Pert	inent Pages	, Etc.)		
27K	Sameshima et al., "XeCl excimer laser annealing used to fabricate poly-si TFTs", Mat. Res. Soc. Symp. Proc., Vol. 71, 1986, pp. 435-440						
DM M	Corey et al., "A shallow junction submicrometer PMOS process without high temperature anneals", IEEE electron device letters, Vol. 9, No. 10, Oct. 1988, pp. 542-544						
GIRM	"Fabrication of Submicrometer MOSFET's Using Gas Immersion Laser Doping (GILD)", IEEE Electron Device Letters, Vol. EDL-7, No. 7, pp. 440-442						
Examiner	G. MUNSON		Date Considered	30 DEC	EMBER 19	97	

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and not considered. Include copy of this form with next communication to applicant.